



11-07-05

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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT		Docket Number: 10746/32	
Application Number 10/092,089	Filing Date March 5, 2002	Examiner Linda Wong	Art Unit 2634
Title CLOCK/DATA RECOVERY CIRCUIT		Applicant(s) KISHINE et al.	

Address to:
Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

1. In accordance with the continuing duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorney(s) for Applicant(s) hereby bring the reference(s) listed on the attached modified PTO Form No. 1449 to the attention of the Examiner. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
2. A copy of each patent, publication or other information listed on the modified PTO form 1449 is enclosed, except as otherwise indicated.
3. Pursuant to 37 C.F.R. § 1.97(c) and § 1.97(e), Applicants' attorney certifies that each item of information contained in this Supplemental Information Disclosure Statement was first cited in a communication dated October 13, 2005 from a foreign patent office in a counterpart foreign (Europe) patent application not more than three months prior to the filing of this Supplemental Information Disclosure Statement.
4. It is believed that no fees are due in connection with this Supplemental Information Disclosure Statement. However, should any fees be due, the Commissioner is authorized to charge Deposit Account No. 11-0600 for such fees. A duplicate copy of this communication is enclosed for charging purposes.

Dated:

11/4/2005

By:

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT PTO-1449 FORM	ATTY. DOCKET NO. 10746/32	U.S. SERIAL NO. 10/092,089
	APPLICANT KISHINE et al.	
	FILING DATE March 5, 2002	GROUP 2634

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	10-285150	October 23, 1998	Japan			Abstract	

OTHER DOCUMENTS

EXAMINER INITIAL		AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
		Müllner et al., "A 20 Gbit/s Parallel Phase Detector and Demultiplexer Circuit in a Production Silicon Bipolar Technology with $f_T=25$ GHz", BIPOLAR/BICMOS Circuits and Technology Meeting, IEEE, 29 September 1996, pp. 43-45.
		J. Savoj, B. Razavi, "A 10 Gb/s CMOS Clock and Data Recovery Circuit", 2000 Symposium on VLSI Circuits Digest of Technical Papers, pp.136-139.
		International Search Report, EP 02 25 1533, October 13, 2005

EXAMINER	DATE CONSIDERED
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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